

PATENT



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Date: 11/21/97 By: *Patricia K. Parry*  
Patricia K. Parry

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

*12/4/97*  
*16*

In re application of:	) Examiner: D. Eng
Charles H. Moore et al.	)
Serial No. 08/484,935	) Group Art Unit: 2315
Filed: June 7, 1995	)
For: HIGH PERFORMANCE	) Palo Alto, CA
MICROPROCESSOR USING	)
INSTRUCTION GROUPS	)

AMENDMENT

Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

Sir:

In response to the Office Action dated August 22, 1997, please amend the above application as follows:

In the Claims:

Rewrite claims 71-75, 78-85, 91-95 and 97-100 as follows:

*E (Sub 71)*

71 (Three Times Amended). A microprocessor system comprising:  
a central processing unit;  
memory;  
a bus connecting said central processing unit to said memory;